MIDES: A Tool for Supervisor Synthesis via Active Learning

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Outline

Problem Statement

2 Active Learning

3 MIDES

4 Learning from PLC

5 Modular Learning

6 Learning a model of LSM

Conclusion and Future Steps

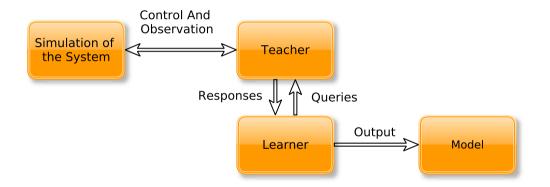
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Supervisory Control Theory relies on access to models of plant and specifications to calculate supervisors.

Obtaining plant models is usually a bottleneck:

- Manually constructing them is hard, time-consuming, and error-prone.
- Manual construction of models is potentially intractable for large and complex systems.
- Once constructed, these models, need to be maintained to reflect the behavior of the real system.



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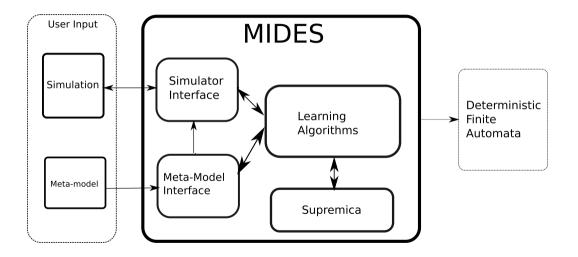
A opensource tool for automatic learning of models and supervisors for discrete event systems using active learning.

Aims

- Ability to prototype and test multiple learning algorithms quickly.
- Easy to interface a variety of external simulation tools.
- Generic output that can be used with existing tools within the supervisory context.

¹https://github.com/ashfaqfarooqui/MIDES

MIDES: Model Inference for Discrete Event Systems



Algorithms

- L* ª
- SupL^{* b}
- Modular Plant Learner ^c
- Modular Supervisor Learner ^d

^aAngluin, Dana. "Learning regular sets from queries and counterexamples." Information and computation 75.2 (1987): 87-106.

^bFarooqui, Ashfaq, Ramon Claase, Martin Fabian, "On Plant-Free Active Learning of Supervisors" Submitted IEEE-TASE

^cFarooqui, Ashfaq, Fredrik Hagebring, and Martin Fabian. "Active learning of modular plant models." IFAC-PapersOnLine 53.4 (2020): 296-302.

^dHagebring, Fredrik, Ashfaq Farooqui, and Martin Fabian. "Modular Supervisory Synthesis for Unknown Plant Models Using Active Learning." IFAC-PapersOnLine 53.4 (2020): 324-330.

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Simulation Interfaces

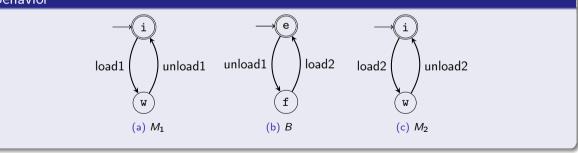
- Internal simulator using variables, predicates, and actions.
- Interface to MATAB engine to simulate MATLAB functions.
- OPC-UA based interface for PLC systems.

Machine Buffer Machine

Product Flow

$$\xrightarrow{\text{load1}} M_1 \xrightarrow{\text{unload1}} B \xrightarrow{\text{load2}} M_2 \xrightarrow{\text{unload2}}$$

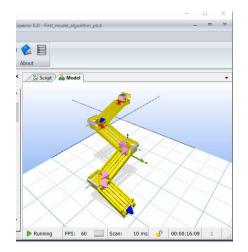
Behavior



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Simulation



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Example learning from PLC

Learning Experior MBM

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Learning outcome

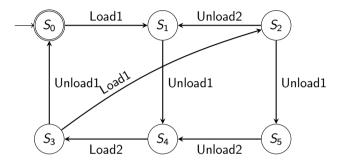
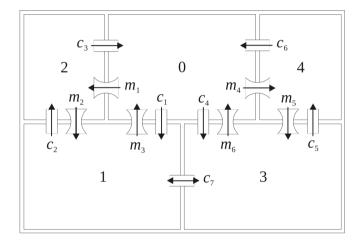


Figure: The maximally permissive controllable and non-blocking learnt supervisor

- Faroogui, Ashfag, Ramon Claase, Martin Fabian, "On Plant-Free Active Learning of Supervisors" Submitted ۲ IEEE-TASE
- Farooqui, Ashfaq, and Martin Fabian. "Synthesis of supervisors for unknown plant models using active learning." 2019 IEEE 15th International Conference on Automation Science and Engineering (CASE). IEEE, 2019.

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Using the internal simulator

```
override val guards: Map[Command, Predicate] = Map(
     c1 \rightarrow EQ(cat, R0).
     c2 \rightarrow EQ(cat, R1).
     c3 \rightarrow EQ(cat, R2).
     c4 \rightarrow EQ(cat, R0).
     c5 \rightarrow EQ(cat, R3),
     c6 \rightarrow EQ(cat, R4).
     c7 \rightarrow OR(EQ(cat, R1), EQ(cat, R3)),
     m1 \rightarrow EQ(mouse, RO).
     m2 \rightarrow EQ(mouse, R2).
     m3 \rightarrow EQ(mouse, R1).
     m4 \rightarrow EQ(mouse, R0).
     m5 \rightarrow EQ(mouse, R4).
     m6 \rightarrow EQ(mouse, R3)
```

override val actions: Map[Command, List[Action]] = Map(c1 -> List(Assign(cat, R1)), c2 -> List(Assign(cat, R2)), c3 -> List(Assign(cat, RO)), c4 -> List(Assign(cat, R3)), c5 -> List(Assign(cat, R4)), c6 -> List(Assign(cat, RO)), c7 -> List(ToggleWithValues(cat, (R1, R3))), m1 -> List(Assign(mouse, R2)), m2 -> List(Assign(mouse, R1)). m3 -> List(Assign(mouse, R0)), m4 -> List(Assign(mouse, R4)). m5 -> List(Assign(mouse, R3)), m6 -> List(Assign(mouse, RO))

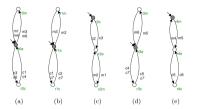


Fig. 2. Specifications for the different rooms $(Kr_0, Kr_1, Kr_2, Kr_3, Kr_4, \text{ in that order})$ ensuring that only one of either the cat or the mouse can be present at a given time. Each state is identified using a unique name.

•
$$\Sigma_{\kappa_1} = \{c_1, c_3, c_4, c_6, m_1, m_3, m_4, m_6\},$$

• $\Sigma_{\kappa_1} = \{c_1, c_2, c_7, m_2, m_3\},$
• $\Sigma_{\kappa_2} = \{c_2, c_3, m_1, m_2\},$
• $\Sigma_{\kappa_3} = \{c_4, c_5, c_7, m_5, m_6\},$
• $\Sigma_{\kappa_4} = \{c_5, c_6, m_4, m_5\},$

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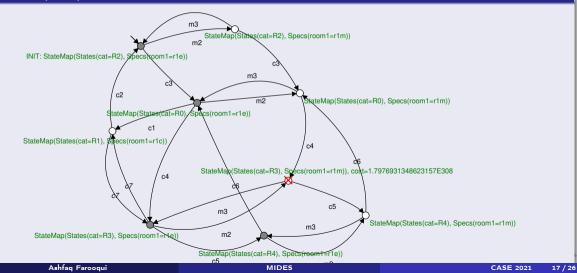
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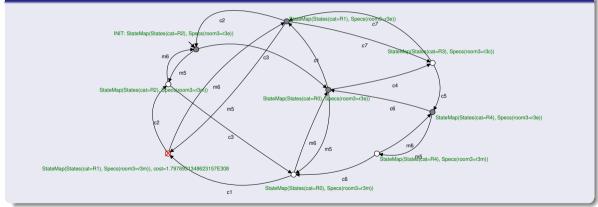
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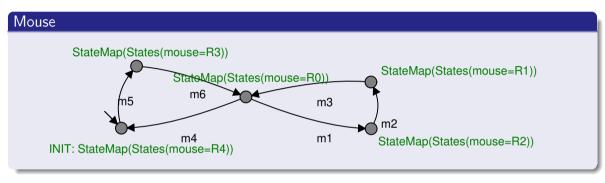
room 1 (M_{K1})



room 3 (M_{K3})



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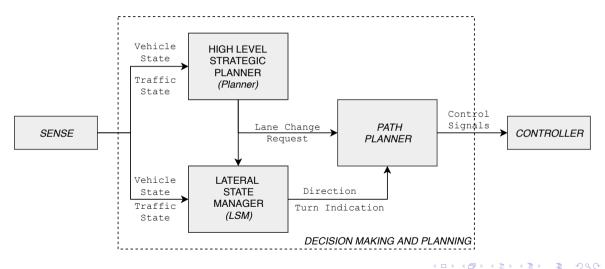


The resulting supervisors (and plants) can then be used in existing tools to generate a maximally permissive controllable and non-blocking supervisor.

- Hagebring, Fredrik, Ashfaq Farooqui, and Martin Fabian. "Modular Supervisory Synthesis for Unknown Plant Models Using Active Learning." IFAC-PapersOnLine 53.4 (2020): 324-330.
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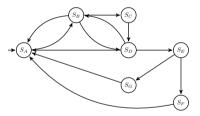
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The LSM

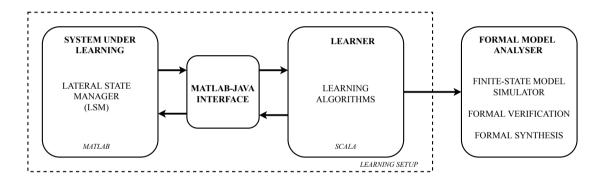


²Selvaraj, Yuvaraj, et al. "Automatically learning formal models: an industrial case from autonomous driving development." Proceedings of the 23rd ACM/IEEE International Conference on Model Driven Engineering Languages and Systems: Companion Proceedings. 2020.

- LSM is programmed in MATLAB
- Inputs from other sub-components are abstracted
- Functionality to run and observe LSM by the learner is introduced



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- The interface provides a standard API to run and observe the SUL
- Provide information to the learner to interpret the observed information



- MPL manages to learn a model that was validated with good confidence
- The obtained model was simulated alongside the original code to check correctness

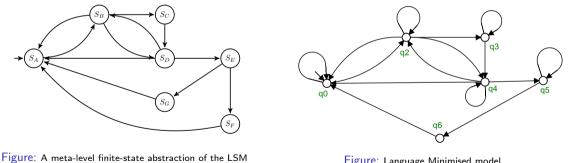


Figure: Language Minimised model

• MIDES, a tool for automatically learning supervisors in the absence of traditional plant models.

• Support for interfacing external simulation environment, eg, MATLAB, OPC-UA.

Future steps

- Improving algorithmic performance using better data-structures.
- Support for richer formalisms Extended Finite Automata, Register Automata, Etc
- Automata simulators

Thank You!

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